

Fig. 1

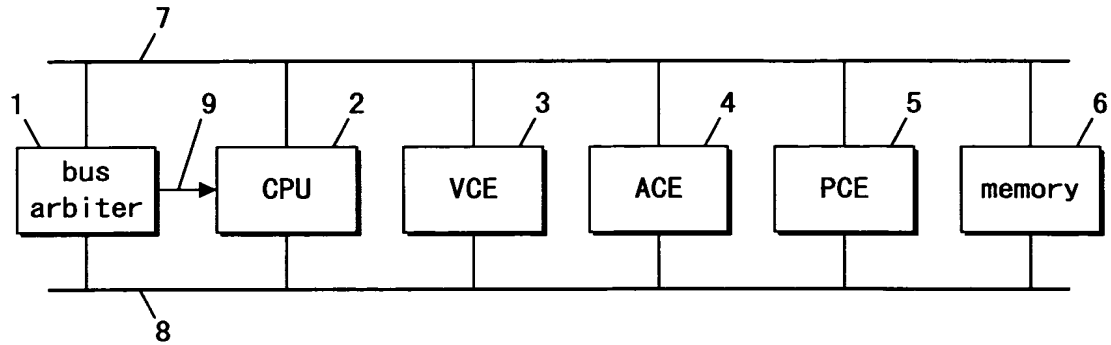


Fig. 2

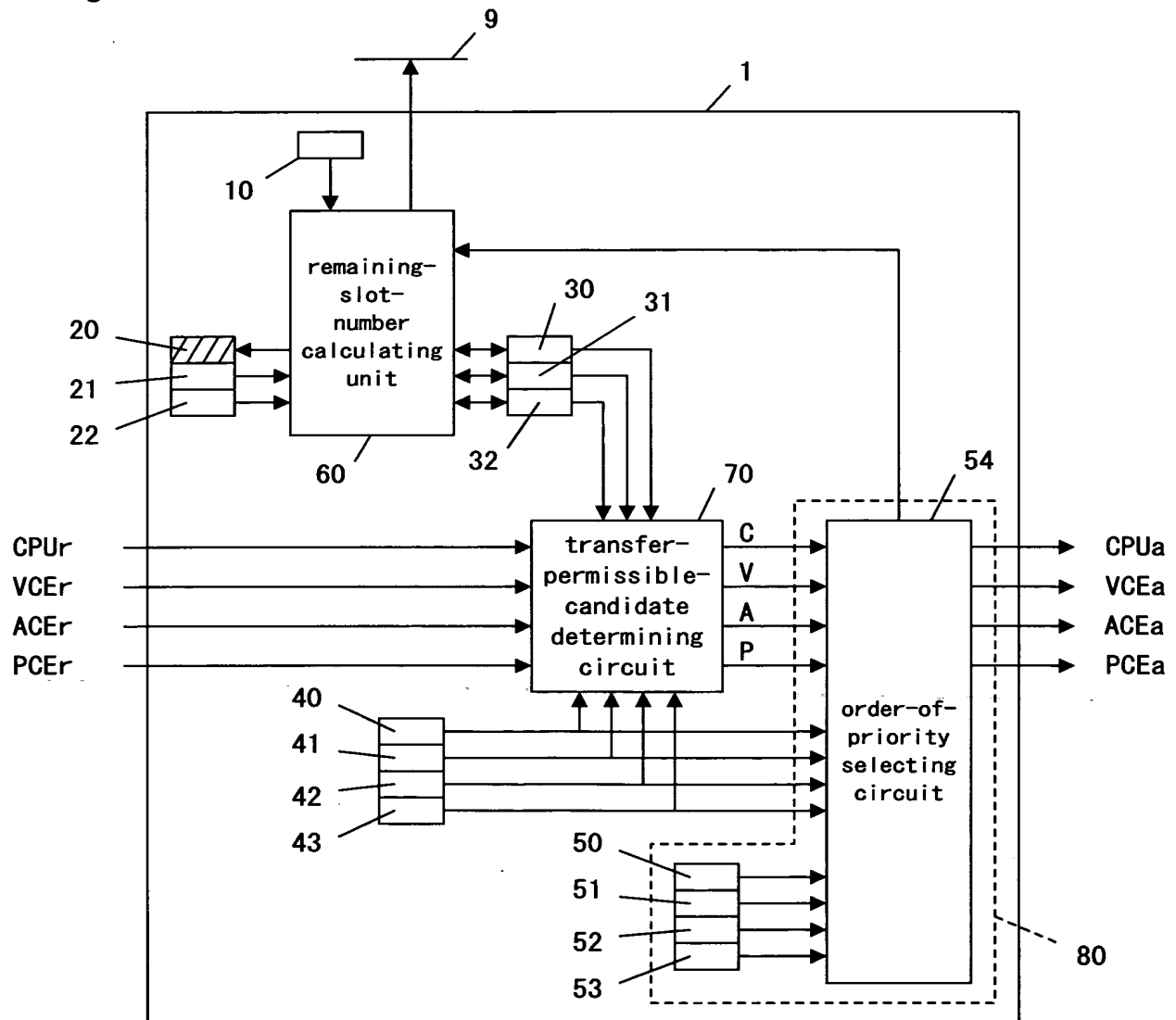


Fig. 3

|                                     |    |
|-------------------------------------|----|
| slot allotment period register 10   | 10 |
| reserved-slot register 21           | 3  |
| reserved-slot register 22           | 2  |
| remaining-reserved-slot register 20 | 5  |

Fig. 4

|  |   |
|--|---|
| renewed-slot designating register 40 for CPU | 0 |
| renewed-slot designating register 41 for VCE | 1 |
| renewed-slot designating register 42 for ACE | 2 |
| renewed-slot designating register 43 for PCE | 0 |

Fig. 5

|                              |   |
|------------------------------|---|
| priority register 50 for CPU | 2 |
| priority register 51 for VCE | 1 |
| priority register 52 for ACE | 0 |
| priority register 53 for PCE | 3 |

Fig. 6

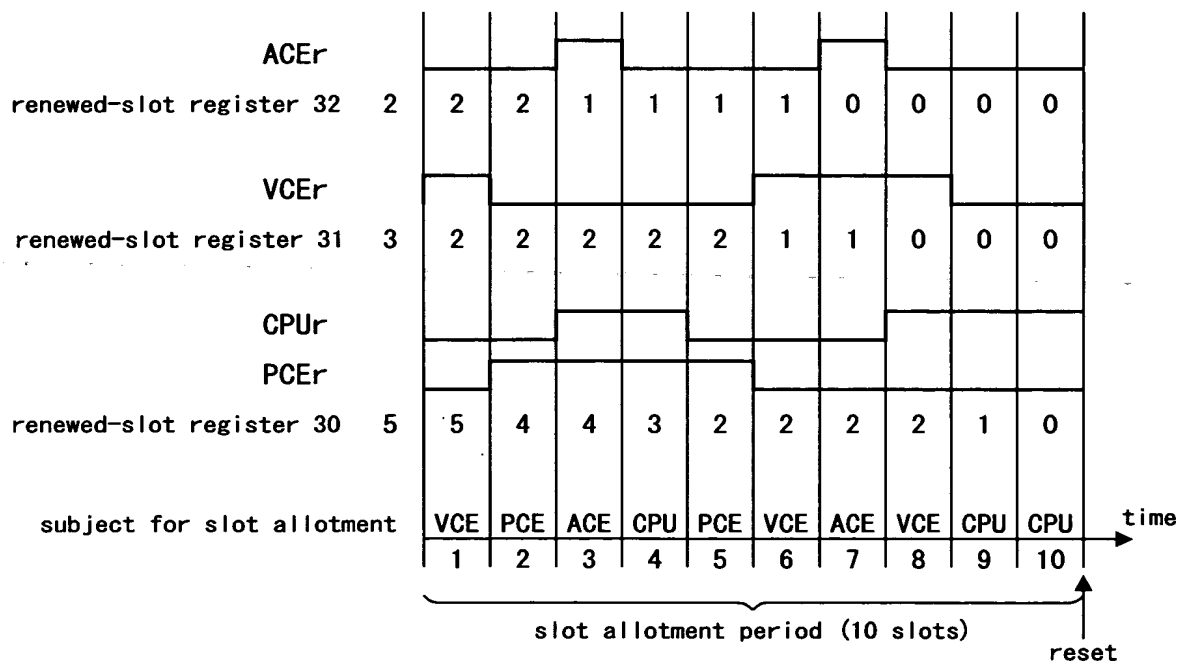


Fig. 7

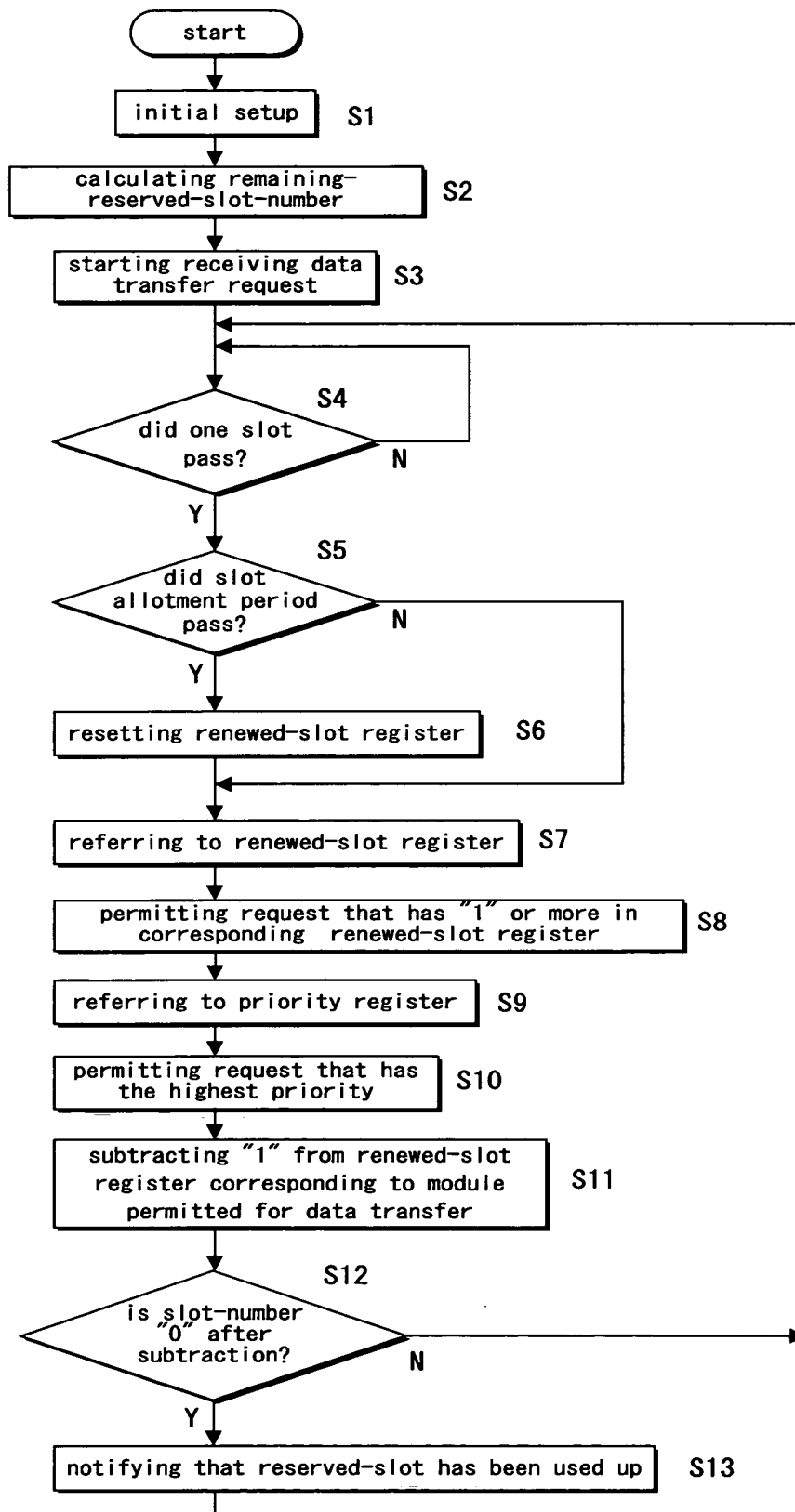


Fig. 8

